

George Souliotis

Assistant Professor

George Souliotis received the B.Sc. degree in Physics from the University of Ioannina, Greece in 1993 and the M.Sc. and Ph.D. degrees in Electronics from the University of Patras, Greece, in 1998 and 2003, respectively.

From 2002 to 2015 was an Adjunct Lecturer at the Department of Electrical Engineering, TEI of Western Greece, Patras, Greece, and he became Lecturer in 2015. From 2004 to 2015 he was with the Department of Physics, University of Patras, Greece as a researcher and laboratory teaching staff. Currently, he is Assistant Professor at the Department of Electrical and Computer Engineering, University of Peloponnese, Patras, Greece.

He has more than twenty years professional and research experience in the design of analog integrated circuits and systems. He has worked for multinational microelectronic companies and still serves as a consultant to companies in Greece and abroad. His research interests include analog integrated circuits, analog filters, high speed serial transceivers (USB2, USB3, M-PHY, BLE), RF circuits up to 80 GHz for wireless transceivers, low voltage, low power electronics and others.

He is author of more than 50 papers in high level journals, conferences and of two patents. He is a reviewer in more than 10 international journals. He is a senior member of IEEE.

Research Interests

Design of analog and mixed signal integrated circuits in CMOS and BiCMOS
Ser/Des, transceivers, drivers, VCO, PLL, phase interpolators, Sense Sampling
Amplifiers, CML circuit
Circuits for USB2, USB3, BLE, M-PHY protocols
Low frequency circuits, BGRs, OpAmps, etc
RF circuits (60GHz-80GHz), mixers, LNAs, PAs
Optoelectronics
Current mode circuits
DC/DC converters